Datasheet

# TP65H050G4WS

650V SuperGaN® FET in TO-247 (source tab)

## Absolute Maximum Ratings

## Circuit Implementation

Gate Ferrite Bead (FB1)

## Electrical Parameters

| Symbol | Parameter | Min | Typ | Max | Unit | Test Conditions |
|---|---|---|---|---|---|---|
| **F a D C a a** | | | | | | |
| | | | | | | |
| | | | | | | |
| | | | | | | |

## Electrical Parameters

| Symbol | Parameter | Min | Typ | Max | Unit | Test Conditions |
|---|---|---|---|---|---|---|
| **R D C a a** | | | | | | |
| | | | | | | ° |
| | | | | | | |
| | | | | | | |

## Typical Characteristics

Figure 1. Typical Output Characteristics $T_J$=25°C

## Typical Characteristics

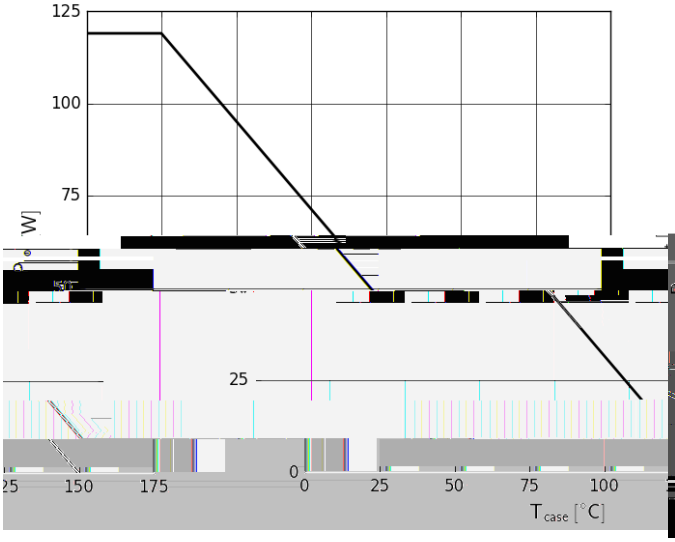



Figure 9. Power Dissipation

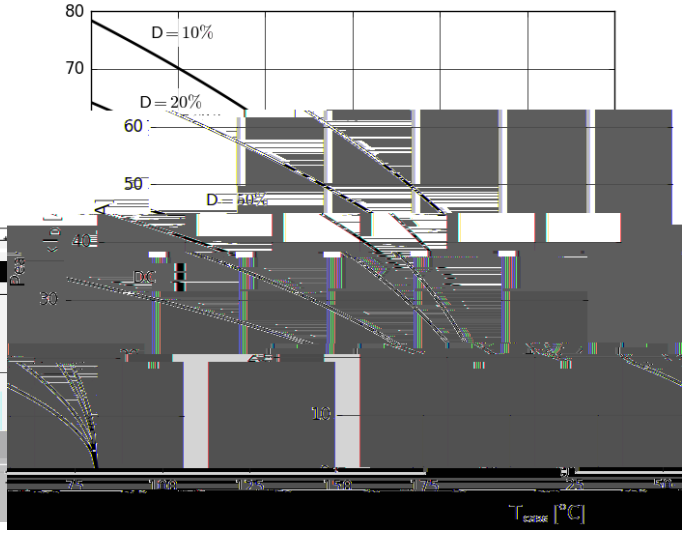



Figure 10. Current Derating

≤




Figure 11. Forward Characteristics of Rev. Diode

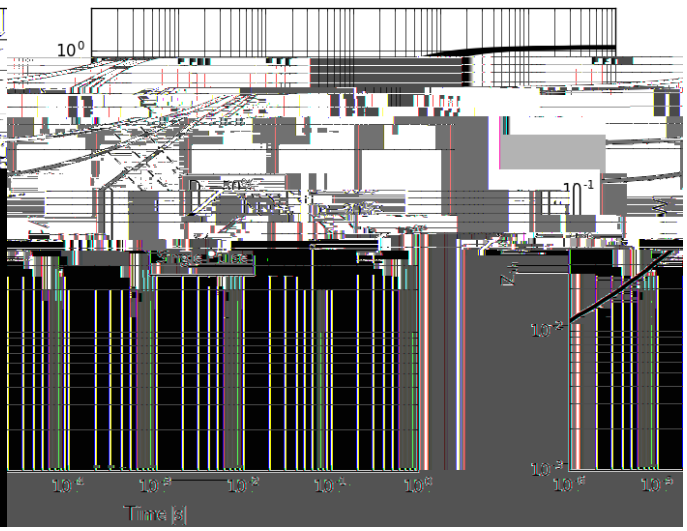



Figure 12. Transient Thermal Resistance

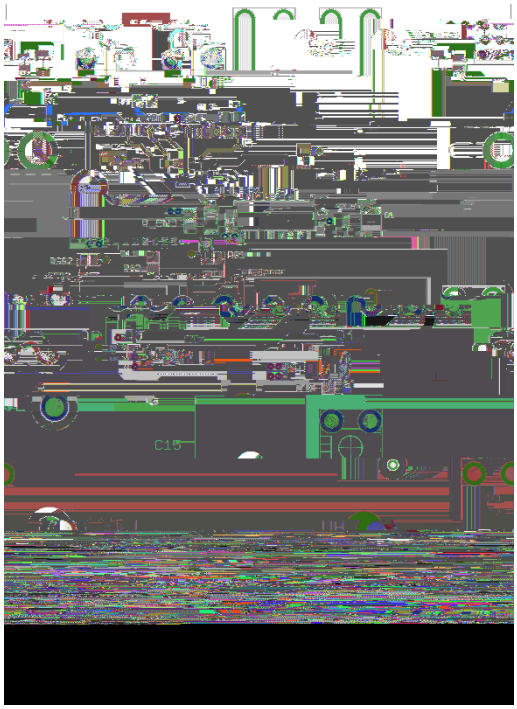
## Typical Characteristics

| | |
|---|---|
| Mechanical | 3 Lead TO |

## Half-bridge Reference Schematic and PCB Layout




Half-bridge layout Sample (Top Layer)



Half-bridge layout Sample (Bottom Layer)

